



PATENT  
8031-1028

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yoshihiro NONAKA

Conf. 5218

Application No. 10/648,256

Group 2811

Filed August 27, 2003

Examiner Ori Nadav

SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF  
MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT,  
CHARGE PUMP CIRCUIT, LAYOUT DESIGNING APPARATUS,  
AND LAYOUT DESIGNING PROGRAM

PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 30, 2005

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of May 3, 2005 for two months to October 3, 2005.

Please charge the extension fee of \$450.00 to Deposit Account No. 25-0120. If this fee is insufficient, the Patent Office is hereby authorized to charge any additional extension fee to Deposit Account No. 25-0120. A duplicate copy of this sheet is enclosed.

A responsive paper is filed herewith.

Respectfully submitted,

YOUNG & THOMPSON

Liam McDowell, Reg. No. 44,231  
745 South 23<sup>rd</sup> Street  
Arlington, VA 22202  
Telephone (703) 521-2297  
Telefax (703) 685-0573  
(703) 979-4709

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